

Mohd Azizi Chik

Senior Manager at SilTerra Malaysia Sdn Bhd

Summary

I give attention to details for the work i am involved in

Experience

Senior Manager at SilTerra Malaysia Sdn Bhd

December 2011 - Present (4 years 1 month)

Manufacturing Systems Focus on Cycle Time Improvement

Senior MTS Industrial Engineering at Silterra Malaysia

January 2008 - December 2011 (4 years)

Besides involve in capacity expansion and benchmarking, I also do key roles to continuously looks for potential improvement to increase available capacity vs cycle time requirements. This include optimizing dispatching policies to meet multiple objective.

Principle Engineer at Chartered Semiconductor

December 2004 - January 2008 (3 years 2 months)

Manufacturing Systems: Supervisor Lim Kian Wee

Engineer at Silterra

2000 - 2004 (4 years)

Skills & Expertise

Semiconductors

Industrial Engineering

Semiconductor Industry

Design of Experiments

IC

CMOS

Engineering

Failure Analysis

Manufacturing

Product Engineering

Cross-functional Team Leadership

Electronics

JMP

Process Improvement

Process Integration
Process Engineering
Project Management
Class Room Training
Industrial Talk at University

Education

Universiti Malaysia Perlis

Doctor of Philosophy (PhD) in Nanoelectronic, Semiconductor Manufacturing, 2010 - 2015

Universiti Kebangsaan Malaysia

Master, Microelectronics, 2003 - 2006

University of Missouri-Columbia

Degree, Industrial Engineering, 1995 - 1998

Activities and Societies: Industrial Engineering

Honors and Awards

1. USPTO Patent 7,672,748 (Chick et al)

Interests

Research in Innovation

Mohd Azizi Chik

Senior Manager at SilTerra Malaysia Sdn Bhd



1 person has recommended Mohd Azizi

"Mohd Azizi is a very detailed oriented person."

— **Bahman D.**, reported to Mohd Azizi at Silterra Malaysia

[Contact Mohd Azizi on LinkedIn](#)